driver TFTs formed over said TFT substrate and forming a driver circuit for driving said pixel TFTs;

a layer of a liquid crystal material with which said pixel TFTs and driver TFTs are in contact directly or via a thin film;

a counter substrate located opposite to said TFT substrate;

a nonconductive or weakly conductive material applied or adhesively bonded to a side edge of said counter substrate and a side edge of ~~said~~ TFT substrate;

a sealing material provided between said TFT substrate and said counter substrate and inside said side edge of said counter substrate and said side edge of said TFT substrate; and

a control circuit provided [within] under and in contact with said sealing material.

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21. (Amended) An active matrix liquid crystal display comprising:

a plurality of pixel TFTs arranged in rows and columns over a TFT substrate and ~~arrayed~~ in a matrix;

driver TFTs formed over said TFT substrate and forming a driver circuit for driving said pixel TFTs;

a layer of a liquid crystal material with which said pixel TFTs and driver TFTs are in contact directly or via a thin film;

a counter substrate located opposite to said TFT substrate;

a nonconductive or weakly conductive material applied or adhesively bonded to a side edge of said counter substrate and a side edge of said TFT substrate;

a sealing material provided between said TFT substrate and said counter substrate and inside said side edge of said counter substrate and said side edge of said TFT substrate; and

a control circuit provided [within] under and in contact with said sealing material for controlling said driver circuit.

22. (Amended) An active matrix liquid crystal display comprising:

a plurality of pixel TFTs arranged in rows and columns over a TFT substrate and arrayed in a matrix;

driver TFTs formed over said TFT substrate and forming a driver circuit for driving said pixel TFTs;

a layer of a liquid crystal material with which said pixel TFTs and driver TFTs are in contact directly or via a thin film;

a counter substrate located opposite to said TFT substrate;

a nonconductive or weakly conductive material applied or adhesively bonded to a side edge of said counter substrate and a side edge of said TFT substrate;

a sealing material provided between said TFT substrate and said counter substrate and inside said side edge of said counter substrate and said side edge of said TFT substrate, said sealing material being provided outside at least said pixel TFTs; and

a control circuit provided [within] under and in contact with said sealing material for controlling said driver circuit.

23. (Amended) An active matrix liquid crystal display comprising:

a plurality of pixel TFTs arranged in rows and columns over a TFT substrate and arrayed in a matrix;

driver TFTs formed over said TFT substrate and forming a driver circuit for driving said pixel TFTs;

a layer of a ~~liquid~~ crystal material with which said pixel TFTs and driver TFTs are in contact directly or via a thin film;

a counter substrate located opposite to said TFT substrate;

a nonconductive or weakly conductive material applied or adhesively bonded to a side edge of said counter substrate and a side edge of said TFT substrate;

a sealing material provided between said TFT substrate and said counter substrate and inside said side edge of said counter substrate and said side edge of said TFT substrate, said sealing

material being provided outside said pixel TFTs and said driver TFTs; and

a control circuit provided [within] under and in contact with said sealing material for controlling said driver circuit.

24. (Amended) A method of fabricating an active matrix liquid crystal display comprising:

a plurality of pixel TFTs arranged in rows and columns over a TFT substrate and arrayed in a matrix;

driver TFTs formed over said TFT substrate and forming a driver circuit for driving said pixel TFTs;

a layer of a liquid crystal material with which said pixel TFTs and driver TFTs are in contact directly or via a thin film;

a counter substrate located opposite to said TFT substrate;

a sealing material provided between said TFT substrate and said counter substrate and outside at least said pixel TFTs; and

a control circuit provided [within] under and in contact with said sealing material for controlling said driver circuit,

said method comprising:

cutting said TFT substrate and said counter substrate outside said sealing material having said control circuit [within] under and in contact with said sealing material; and

applying or adhesively bonding a nonconductive or weakly conductive material to the cut side edge of said TFT substrate and the cut side edge of said counter substrate.

25. (Amended) A method of fabricating an active matrix liquid crystal display comprising:

a plurality of pixel TFTs arranged in rows and columns over a TFT substrate and arrayed in a matrix;

driver TFTs formed over said TFT substrate and forming a driver circuit for driving said pixel TFTs;

a layer of a liquid crystal material with which said pixel TFTs and driver TFTs are in contact directly or via a thin film;

a counter substrate located opposite to said TFT substrate;

a sealing material provided between said TFT substrate and said counter substrate and outside said pixel TFTs and said driver TFTs; and

a control circuit provided [within] under and in contact with said sealing material for controlling said driver circuit,

said method comprising:

cutting said TFT substrate and said counter substrate outside said sealing material having said control circuit [within] under and in contact with said sealing material; and

applying or adhesively bonding a nonconductive or weakly conductive material to the cut side edge of said TFT substrate and the cut side edge of said counter substrate.

Please add the following new claims.

-- 57. (New) An active matrix liquid crystal display comprising:

a plurality of pixel TFTs arranged in rows and columns over a TFT substrate and arrayed in a matrix;

driver TFTs formed over said TFT substrate and forming a driver circuit for driving said pixel TFTs;

a layer of a liquid crystal material with which said pixel TFTs and driver TFTs are in contact directly or via a thin film;

a counter substrate located opposite to said TFT substrate;

a sealing material provided between said TFT substrate and said counter substrate and inside a side edge of said counter substrate and a side edge of said TFT substrate; and

a control circuit provided under and in contact with said sealing material.

58. (New) An active matrix liquid crystal display comprising:

a plurality of pixel TFTs arranged in rows and columns over a TFT substrate and arrayed in a matrix;

driver TFTs formed over said TFT substrate and forming a driver circuit for driving said pixel TFTs;

a layer of a liquid crystal material with which said pixel TFTs and driver TFTs are in contact directly or via a thin film;

a counter substrate located opposite to said TFT substrate;

a sealing material provided between said TFT substrate and said counter substrate and inside a side edge of said counter substrate and a side edge of said TFT substrate; and

a control circuit provided under and in contact with said sealing material for controlling said driver circuit.

59. (New) An active matrix liquid crystal display comprising:

a plurality of pixel TFTs arranged in rows and columns over a TFT substrate and arrayed in a matrix;

driver TFTs formed over said TFT substrate and forming a driver circuit for driving said pixel TFTs;

a layer of a liquid crystal material with which said pixel TFTs and driver TFTs are in contact directly or via a thin film;

a counter substrate located opposite to said TFT substrate;

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a sealing material provided between said TFT substrate and said counter substrate and inside a side edge of said counter substrate and a side edge of said TFT substrate, said sealing material being provided outside at least said pixel TFTs; and

a control circuit provided under and in contact with said sealing material for controlling said driver circuit.

60. (New) An active matrix liquid crystal display comprising:

a plurality of pixel TFTs arranged in rows and columns over a TFT substrate and arrayed in a matrix;

driver TFTs formed over said TFT substrate and forming a driver circuit for driving said pixel TFTs;

a layer of a liquid crystal material with which said pixel TFTs and driver TFTs are in contact directly or via a thin film;

a counter substrate located opposite to said TFT substrate;

a sealing material provided between said TFT substrate and said counter substrate and inside a side edge of said counter substrate and a side edge of said TFT substrate, said sealing material being provided outside said pixel TFTs and said driver TFTs; and

a control circuit provided under and in contact with said sealing material for controlling said driver circuit.--